AMENDMENTS TO THE SPECIFICATION:

Please replace the paragraph starting at Page 16, line 16 with the following amended paragraph:

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-- In one embodiment, microprograms and portal data 560 565 are transmitted from the portal server 110 to the external memory 550 of the portal device via a communication interface 570 under control of the CPU 510. Various communication interfaces 570 may be employed without departing from the underlying principles of the invention including, for example, a Universal Serial Bus ("USB") interface or a serial communication ("serial") interface. The microprograms in one embodiment are comprised of compact, interpreted instructions known as "bytecodes," which are converted into native code by the interpreter module 520 before being executed by the CPU 510. One of the benefits of this configuration is that when the microcontroller/CPU portion of the portal device 150 is upgraded (e.g., to a faster and/or less expensive model), only the interpreter module 520 and toolbox 530 of the ROM needs to be rewritten to interpret the currently existing bytecodes for the new microcontroller/CPU. In addition, this configuration allows portal devices 150 with different CPUs to coexist and execute the same microprograms. Moreover, programming frequently-used routines in the ROM toolbox module 530 reduces the size of microprograms stored in the external memory 550, thereby conserving memory and bandwidth over the client link 160 and/or the RF link 220. In one embodiment, new interpreter modules 520 and/or toolbox routines 530 may be developed to execute the same microprograms on cellular phones, personal information managers ("PIMs"), or any other device with a CPU and memory.